## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

David CASEY

Title:

**BIPOLAR TRANSISTOR WITH** 

A LOW SATURATION

VOLTAGE

Appl. No.:

10/566,813

International

07/12/2004

Filing Date:

371(c) Date:

01/30/06

Examiner:

Jae Lee

Art Unit:

2823

Confirmation

8053

Number:

## **PETITION FOR EXTENSION OF TIME**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby petitions the Commissioner under 37 C.F.R. §1.136(a) for a three-month extension of time for response in the above-identified application for the period required to make the attached response timely.

The extension fee for response within the third month is \$1,050.00.

The above-identified fees of \$1,050.00 are being paid by credit card via EFS-Web.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to

Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment instructions in EFS-Web being incorrect or absent, resulting in a rejected or incorrect credit card transaction, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

FOLEY & LARDNER LLP

Customer Number: 23392

Telephone: (213) 972-4594 Facsimile: (213) 486-0065 By At St Reg. No. 56,252

Ted R. Rittmaster
Attorney for Applicant
Registration No. 32,933